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04002-US

FIG.1

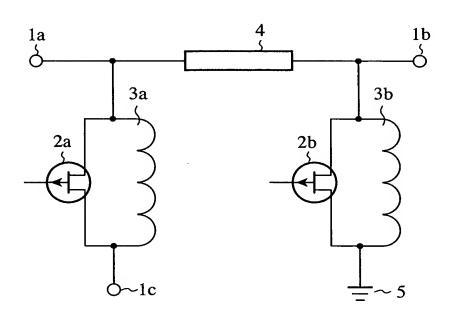
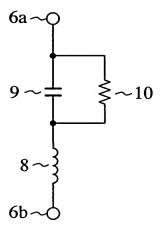


FIG.2



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FIG.3



FIG.4 **Q~11a** 14b 5 14a 13b 13a 12b 12a **Ó~**11b

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FIG.5

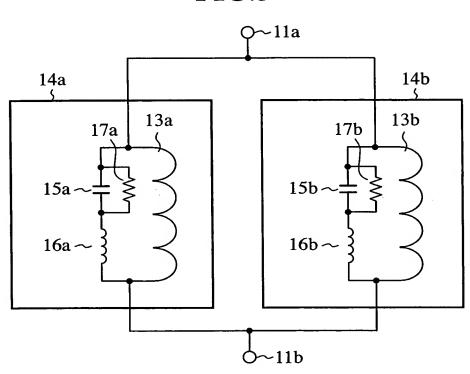
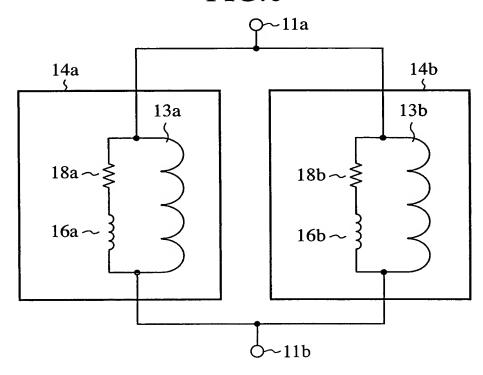
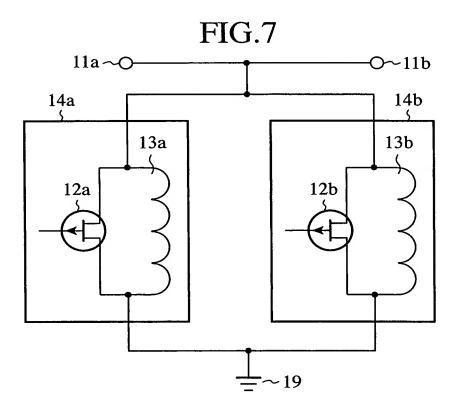


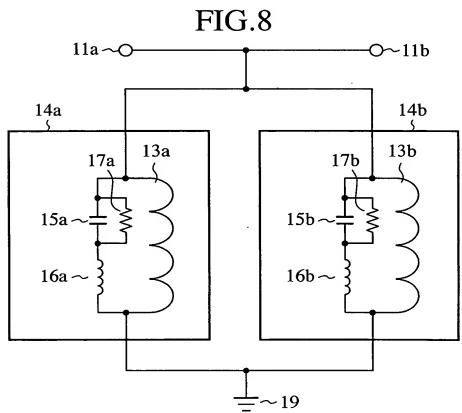
FIG.6



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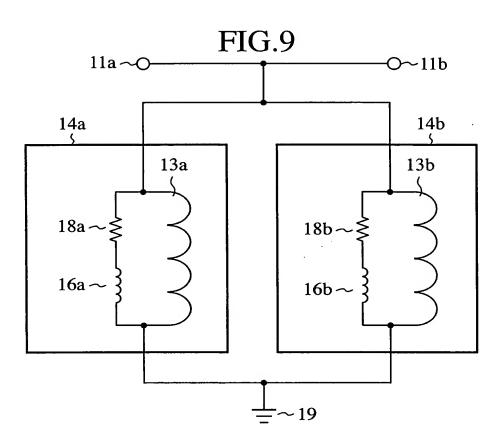
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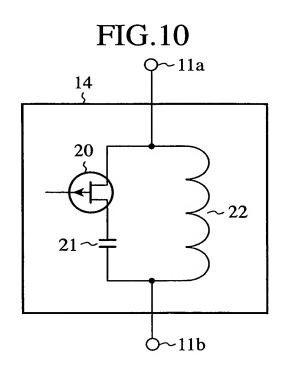




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FIG.11

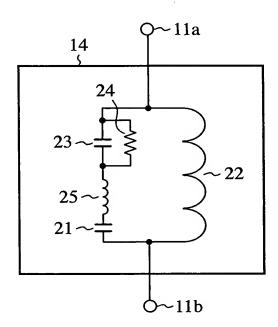
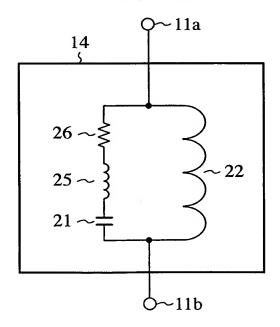


FIG.12



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FIG.13

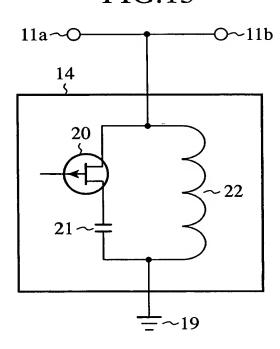
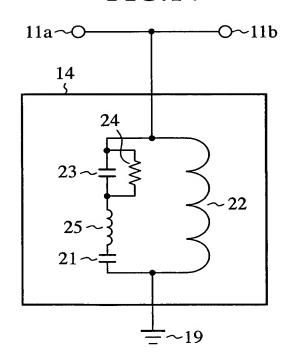


FIG.14



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FIG.15

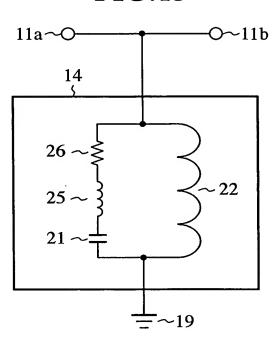
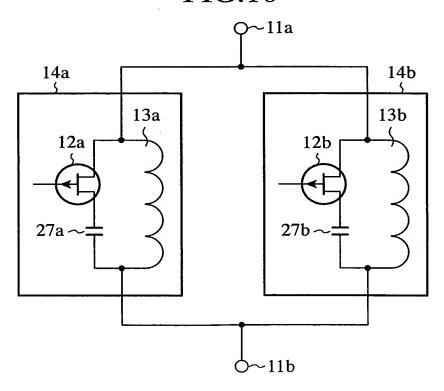


FIG.16



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FIG.17

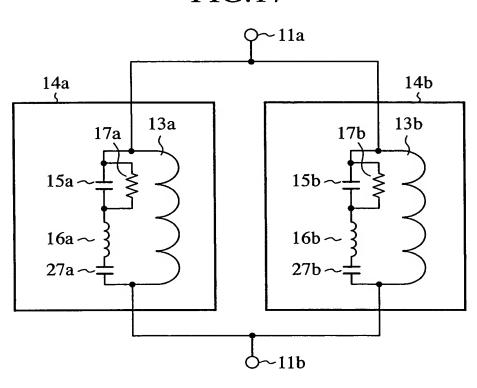


FIG.18 Q~11a 14a 14b 13a 13b 18a -18b-16a ~ 16b-27a 27b **Ó**~11b

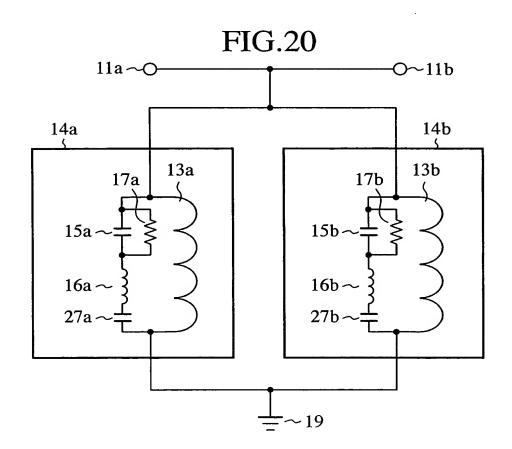
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FIG.19 **○**~11b 11a **~**○ 14a 14b 13a 13b 12b 12a 27a ∼ 27b-

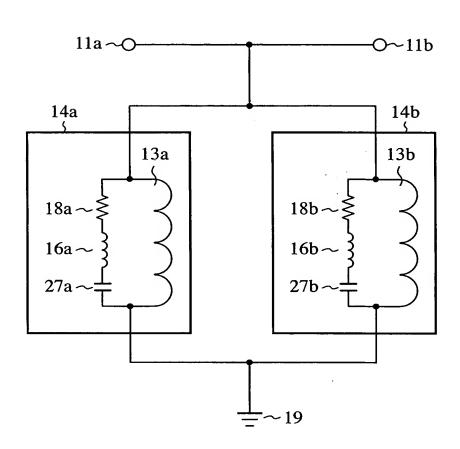
~19



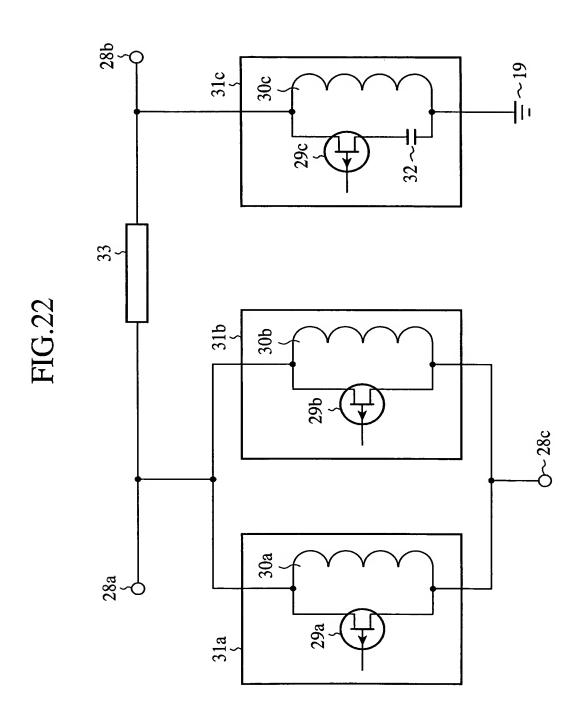
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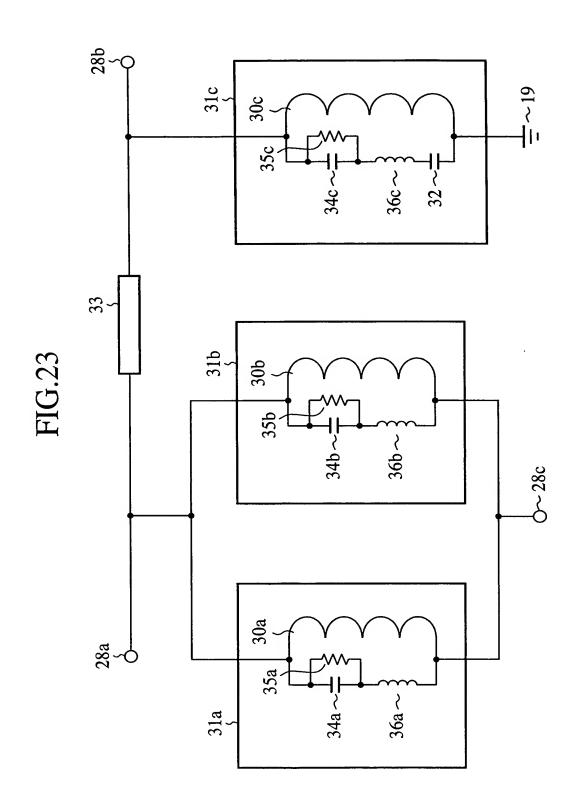
FIG.21



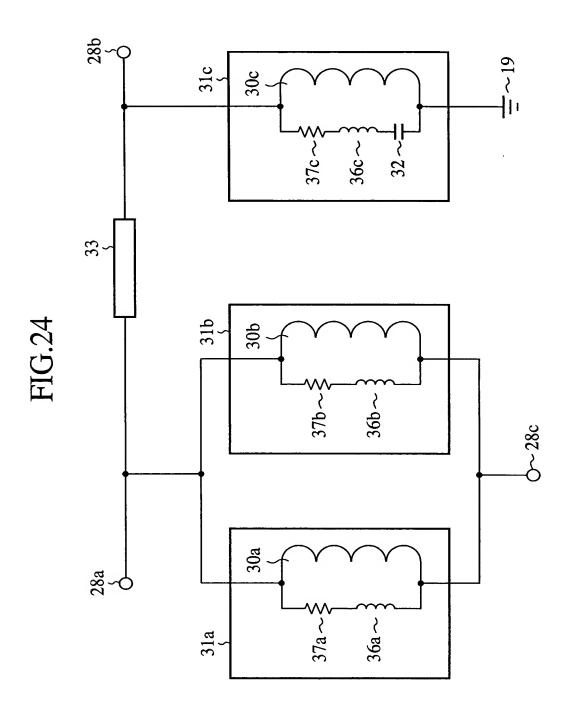
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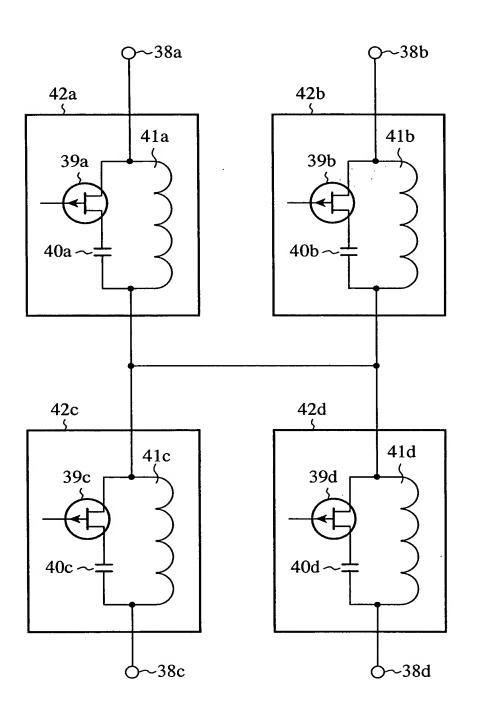
App No.: NEW Docket No.: 1163-0573PUS1 Inventor: Masatake HANGAI et al. Title: SPST SWITCH, SPDT SWITCH AND MPMT SWITCH NEW SHEET Sheet 14 of 16



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FIG.25



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FIG.26

Input terminal or output terminal	Output terminal or input terminal	FET39a	FET39b	FET39c	FET39d
38a	38b	ON	ON	OFF	OFF
	38c	ON	OFF	ON	OFF
	38d	ON	OFF	OFF	ON
38b	38c	OFF	ON	ON	OFF
	38d	OFF	ON	OFF	ON
38c	38d	OFF	OFF	ON	ON